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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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**Complete if Known**

Application Number	10/534,304
Filing Date	May 9, 2005
First Named Inventor	Wolfgang EINBRODT
Art Unit	
Examiner Name	
Attorney Docket Number	D4695-00133

Sheet	1	of	2
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## U.S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
	----	PCT Int'l Search Report	02-16-2005		for information only	
	----	German Search Report	05-25-2004		for information only	
		JP09148617A2	06-06-1997	Satoshi		
		JP63174358A2	07-18-1988	Kayao		
		WO 02/067339A1	08-29-2002	Yang, et. al.		
		WO 02/49120A1	06-20-2002	Hall, et. al.		
		WO 02/33755A1	04-25-2002	Augusto, et. al.		

Examiner Signature	/Benjamin Tzu-Hung Liu/	Date Considered	12/21/2008
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Sheet

2

2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T <sup>2</sup>
		FOERTSCH, M.; "220 MHz optical receiver with large-area integrated PIN photodiode". Proceedings of IEEE Sensors 2003; Bd. Vol. 2 of 2, Conf. 2.	
		FOERTSCH, M.; "Integrated PIN photodiodes in high-performance BiCMOS technology". Int'l Electron Devices Meeting 2002.	
		HOHENBILD, M.; "Advanced photodiodes and circuits for OPTO-ASICs". 2001 International Symposium on Electron Devices for Microwave and Optoelectronic Devices.	
		KUCHTA, D.; "Performance of fiber-optic data links using 670-nm cw VCSELs and a monolithic Si photodetector and CMOS preamplifier". IBM Jnl. Res. Develop. 39, pp. 63-72, 1995.	
		KYOMASU, M.; "Development of an integrated high speed silicon PIN photodiode sensor". IEEE Trans. On Electron Dev., vol. 42, no. 6 pp. 1093-1099, June 1995.	
		LIM, P.; "A 3.3-V monolithic photodetector/CMOS preamplifier for 532 Mb/s optical data link applications". Digest Technical Papers ISSCC 1993, pp. 96-97.	
		YAMAMOTO, M.; "Si-OEIC with aq built-in pin-photodiode". IEEE Trans. Electron Dev. 42 (1), pp. 58-63, 1995.	
		YANG, M.; "A high-speed, high sensitivity silicon lateral trench photodetector". IEEE Electron Dev. Lett., pp. 395-397, 2002.	
		ZIMMERMAN, H.; "Monolithic high-speed CMOS-photoreceiver". IEEE Photonics Technology Letters 11, pp. 254-256, 1999.	
		ZIMMERMAN, H.; "Monolithic bipolar-, CMOS-, and BiCMOS-receiver OEICs". 1996 International Semiconductor Conference; Bd. Vol. 2, Conf. 19.	

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